

**INTERNAL SCHOLARS**

<b>S.No</b>	<b>Name</b>	<b>Year of Reg.</b>	<b>Reg. No.</b>	<b>Title</b>	<b>Status</b>
1.	Ms.K.Saranya, Assistant Professor -SS/ EEE	2016	16244697151	Design and Implementation of Reversible VLSI Architectures for Image Cryptography	Viva comple ted - 23.11.2 022