

Value Added Courses: 2017-2018

| Date(s) | VAC Course Name | No. of students Benefitted | Resource Persons |
|--------------------------------|---|-----------------------------------|--|
| 17.12.2016 to 22.12.2016 | CMOS Analog IC Design Using Cadence EDA Tool | 26 | Dr.K.N.Vijeyakumar Dr.C.Kalamani Ms.S.Kalaiselvi |
| 13.02.2017 to 17.02.2017 | CMOS Analog IC Design and Recent trends in Physical Design using Cadence EDA Tool | 29 | Dr.K.N.Vijeyakumar Dr.C.Kalamani Ms.S.Kalaiselvi |
| 15-03-2017 to 17-03-2017 | CMOS Analog RF IC Design Using Cadence EDA Tool | 26 | Dr.K.N.Vijeyakumar Dr.C.Kalamani Ms.S.Kalaiselvi |
| 28-06-2017 to 30-06-2017 | CMOS Analog IC Design and Layout Techniques | 26 | Dr.K.N.Vijeyakumar Dr.C.Kalamani Ms.S.Kalaiselvi |
| 16-08-2017 to 18-08-2017 | CMOS Analog IC Design and Layout Techniques using Cadence EDA Tool | 28 | Dr.K.N.Vijeyakumar Dr.C.Kalamani Ms.S.Kalaiselvi |
| 18-01-2018 to 19-01-2018 | CMOS Analog RF IC Design Using Cadence EDA Tool | 33 | Dr.K.N.Vijeyakumar Dr.C.Kalamani Ms.S.Kalaiselvi |
| 02-02-2018 to 03-02-2018 | FPGA Implamentation of preprocessing algorithms for Digital Images | 34 | Dr.K.N.Vijeyakumar Dr.C.Kalamani Ms.S.Kalaiselvi |