OBJECTIVE OF THE WORKSHOP

The objective of the workshop is to bring people engaged in Education, Research & Development together to enhance their knowledge in the field of CMOS analog RF system design and measurement techniques which will help the participants to set new millstones in the above mentioned fields of research for their pursuit.

ORGANIZING COMMITTEE:

Convener:

Dr. R. SUDHAKAR

Professor & Head, ECE

Co-Ordinators:

Dr. K.N. VIJEYAKUMAR

Dr. K. SUMATHI

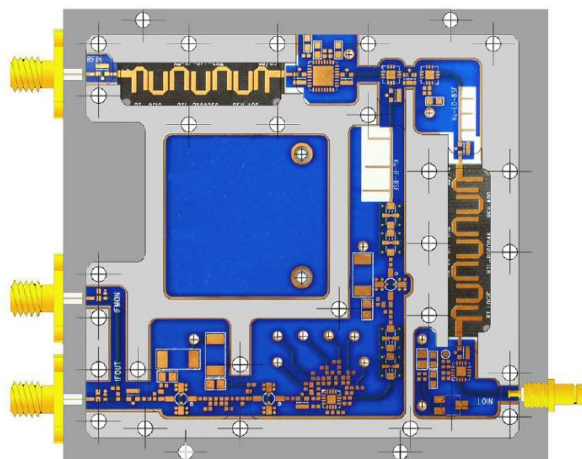Associate Professors, ECE

Organizing Secretary:

Mr. K. DHINESH
Assistant Professor, ECE

Committee Members:

Ms. C. KALAMANI

Ms. D. REKA

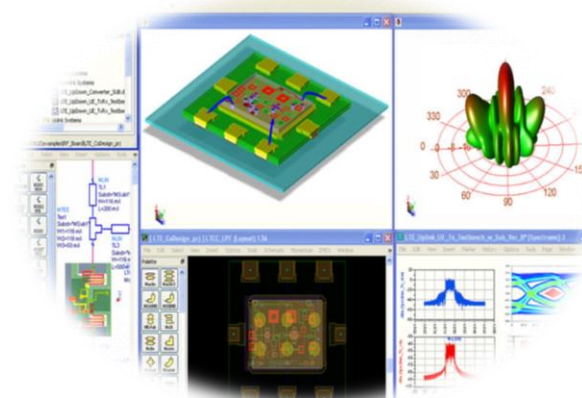Ms. S. KALAISELVI

Ms .G. SWATHIGA

Assistant Professors/ ECE

CONTENTS

- ➢ Microstrip Patch Antenna Design using ADS EDA tool
- ➢ S Parameter Analysis
- ➢ Microwave Filter Design using ADS EDA tool
- ➢ Impedance Matching
- ➢ Low Noise Amplifier design and Parameter Optimization using Cadence EDA tool
- ➢ Active Mixer Design using Cadence EDA tool
- ➢ Receiver System Integration

MCET — COLLEGE OF ENGINEERING AND TECHNOLOGY — Enlightening Technical Minds

KEYSIGHT TECHNOLOGIES

cadence™

Two days workshop on

"CMOS RF Receiver Front End Design using CADENCE and ADS EDA Tools"

26th and 27th September 2017

Organized by
Department of ECE
at
ASIC Centre of Excellence
&
KEYSIGHT Centre for RF Communication Systems

Dr. MAHALINGAM COLLEGE OF ENGINEERING AND TECHNOLOGY

(An Autonomous Institution Accredited by NAAC)

Udumalai Road, M.K. Patti (P.O),

Pollachi - 642003, Tamilnadu.

website: www.drmcet.ac.in

ABOUT THE COLLEGE

Dr. Mahalingam College of Engineering and Technology (MCET) was established in the year of 1998 by Dr. M. MANICKAM with a view to commemorate the 75th birthday of his beloved father Arutchelvar Dr. N. MAHALINGAM with a mission to impart high quality competency based education in Engineering & Technology to the younger generation with the required skills and abilities to face the challenging needs of the industry around the globe. MCET is a self-financing, co-educational Autonomous Engineering College and it is approved by All India Council for Technical Education (AICTE), New Delhi & Affiliated to Anna University, Chennai. The Institution has been accredited by the National Assessment and Accreditation Council (NAAC). MCET currently offers 10 UG, 6 PG and 4 Doctoral programmes in Engineering and Technology.

ABOUT THE DEPARTMENT

The Department of ECE was established in the year 1998. It is accredited by National Board of Accreditation (NBA) and it is also recognized as Research Centre by Anna University, Chennai. Currently the department offers four years Bachelor of Engineering, with an annual intake of 120 students and Master of Engineering in Communication Systems with an annual intake of 18 students. The department has qualified, committed and research oriented faculty members. The department is focusing on research activities in the predominant areas of Communication & Networking, VLSI & Embedded System, Signal processing and Data Analytics. The department also conducts workshops and hands-on training programmes periodically to inculcate research expertise among the students.

ADDRESS FOR COMMUNICATION

Dr. K.N. VIJEYAKUMAR
Department of ECE
Dr. Mahalingam College of Engineering and Technology, Pollachi - 642003
Email: vijey.tn@drmcet.ac.in
Mobile Number: 9942377050/9842110028

REGISTRATION DETAILS

Registration Fee	Amount **
Industry Participants/ Research Scholars/Faculty members	Rs.1000/-
Students (UG & PG)	Rs.500/-

**Demand Draft for the registration fee must be drawn in favour of ""The Principal, Dr. Mahalingam College of Engineering and Technology" payable at Pollachi. The registration fee includes workshop kit, refreshment and lunch.

Accommodation on payable basis will be arranged in the hostel premises on prior intimation.

Duly filled registration form along with Demand Draft must reach us on or before *20th September 2017.*

ELIGIBILITY

Faculty members, Research scholars, Students and Industry experts with ECE / EEE background.

Dr. MAHALINGAM COLLEGE OF ENGINEERING AND TECHNOLOGY
(An Autonomous Institution Accredited by NAAC)

Two days workshop on

"CMOS RF Receiver Front End Design using CADENCE and ADS EDA Tools"

26th and 27th September 2017

REGISTRATION FORM

Name:

Age:

Qualification:

Designation:

Organization:

Address for Communication:
(with telephone no. & E-mail ID)

DD No:

Date:

Bank:

Signature of the Candidate: