## **External Scholars**

| S.No | Candidate Name & Designation                   | Presently working<br>Institution/Industry          | Year of<br>Registration | Title of the Research<br>Work  | Current Status        |
|------|--|--|-------------------------|--|-----------------------|
| 1.   | Mr.D.Kumar ,<br>Assistant Professor /ECE       | PA College of Engineering and Technology, Pollachi | 2015                    | Image Encryption with lossless / Lossy Compression                                     | Thesis Submitted      |
| 2.   | Mr.J.Dhanansekar,<br>Assistant Professor /ECE  | Sri Eshwar College of Engineering, Coimbatore      | 2016                    | ASIC Implementation of RRAM by adiabatic Logic for low power applications              | Synopsis Submitted    |
| 3.   | Mr.Puviarasu A<br>Assistant Professor /ECE     | Sri Eshwar College of Engineering, Coimbatore      | 2022                    | Defense mechanism and mitigation techniques for DOS attacks in wireless sensor network | Doing Course Work     |
| 4.   | Mr. Silambarasan D Assistant Professor / ECE   | Anna University<br>Regional Centre,<br>Coimbatore  | 2015                    | High speed residue system for cryptography applications                                | Confirmation Received |
| 5.   | Mr.Maragatharaj S Assistant<br>Professor / ECE | Knowledge Institute of Technology, Kakapalayam,    | 2016                    | Design of massive input<br>multi output low power<br>variable digital filter           | Synopsis submitted    |

|    |  | Chennai  |      | banks for ultra wide band spectrum sensing in cognitive radio systems.                        |                       |
|----|--|--|------|---|-----------------------|
| 6. | Mr.Nelson Kingsley Joel.P<br>Assistant Professor / ECE | Sri Ranganathan Engineering College, Coimbatore.   | 2015 | Implementation of Efficient Algorithms for Removal of Noises in Digital Images                | Synopsis submitted    |
| 7. | Mr.Lenin Raja R Assistant<br>Professor / ECE           | Sri Vidhya College of Engg. & Tech., Virudhunagar. | 2015 | An Enhanced switching technique for reducing power consumption using multi dynamic node       | Synopsis submitted    |
| 8. | Ms.P.Gnanambikai,<br>Lecturer(SG)/ECE                  | NPTC,Pollachi                                      | 2016 | Design of Efficient VLSI architecture for image De-Noising                                    | Thesis submitted      |
| 9. | K.Sedhu ramalingam Assistant<br>Professor / ECE        | SreeSakthi Engineering<br>College, Coimbatore      | 2017 | Certain Investigations on<br>Network Issues for<br>secured Data Storage in<br>Cloud Computing | Confirmation Received |