

Academic Year: 2014-2015

Date(s)	Type of Programme	Title	No. of Participants attended		Total	Resource Persons
			External	Internal		
05.06.2014 & 06.06.2014	Training Programme For Faculties	IC Design Using Cadence EDA Suite	-	30	30	Technical staff Cadence design Systems(India), Bangalore.
05.01.2015 & 10.01.2015	Short Term Training Programme	Custom Analog IC design using Cadence EDA Suite	11	-	11	Dr.K.N.Vijeyakumar Asso. Prof/EEE Ms.K.Saranya & Ms.M.Sangeetha, APs/EEE
18.02.2015 & 19.02.2015	Workshop	Custom Analog IC Design Using CADENCE EDA Suite	20	-	20	Dr.K.N.Vijeyakumar Asso. Prof/EEE Ms.K.Saranya & Ms.M.Sangeetha, APs/EEE
12.03.2015	Workshop (EEE Association)	CMOS Design Flow Using Microwind	-	30	30	Dr.K.N.Vijeyakumar Asso. Prof/EEE Ms.K.Saranya & Ms.M.Sangeetha, APs/EEE
14.03.2015	Workshop (Uddeshah'15)	Custom Analog IC Design using CADENCE EDA Suite	24	-	24	Dr.K.N.Vijeyakumar Asso. Prof/EEE Ms.K.Saranya & Ms.M.Sangeetha, AP/EE
10.04.2015 & 11.04.2015	Workshop	System design using FPGA and Cadence EDA Tools	-	44	44	Dr.K.N.Vijeyakumar Asso. Prof/EEE Ms.K.Saranya & Ms.M.Sangeetha, AP/EE