

REGISTRATION FORM

TWO DAY WORKSHOP ON SIMULATION OF IEEE TRANSACTION PAPERS ON INDUSTRIAL ELECTRONICS

Name : _____
Designation : _____
Department : _____
Institution : _____
Academic Qualification: _____
Experience (Yrs) : _____
Postal Address : _____

Mobile: _____ E-mail _____

Accommodation required (at extra cost): YES / NO

Payment Details (Please tick whichever is applicable)

Demand Draft Online Transfer

Draft No. / Online Trans.Ref. No: _____

Demand Draft drawn in favour of **MCET IEEE
STUDENTS BRANCH** payable at Pollachi.

(Send the hard copy of the transaction reference details
to the contact address on or before 20.01.18)

Bank Account Details:

Account Name: MCET IEEE STUDENTS BRANCH

Branch : NPT Campus, Pollachi

Account No: 6139 220 0020650

Bank Name : Syndicate Bank

IFSC : SYNB0006139, MICR: 641025020

Date: _____ Signature of the candidate

Mr. / Ms. /Dr. _____ is a
full time employee of our Institute and is hereby
sponsored. He/She will be permitted to attend the
programme, if selected, for the entire duration.

Head of the Institution

ORGANIZING COMMITTEE

Chief Patron:

Dr. M. MANICKAM

Chairman, MCET

Patron:

Shri. M. HARI HARA SUDHAN

Correspondent, MCET

Chairman:

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Co- Chairman:

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Principal, MCET

Organizing Secretary:

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Professor and Head, EEE

Conveners:

Dr. A. SAKTHIVEL

Associate Professor / EEE

Dr. M. KALIAMOORTHY

Associate Professor / EEE

Co-conveners:

Dr. B. VINOTH KUMAR, AP(SG)/EEE

Mr. K. M. MANU, AP / EEE

Coordinators:

Mr. M. SARAVANAKUMAR, AP/EEE

Mr. C. KANNAN, AP/EEE

Mr. T. ANANDKUMAR, AP/EEE

Mr. M. PRABHU RAJ, AP/EEE

Mr. D. GNANAPRAKASAM, AP/EEE

Ms. N. KRISHNAVENI, AP/EEE

Mr. S. DINESH, AP/EEE

Ms. R. L. HELEN CATHERINE, AP/EEE

**Dr. MAHALINGAM COLLEGE OF
ENGINEERING AND TECHNOLOGY
Pollachi-642003**



**IEEE MADRAS SECTION
Sponsored
TWO-DAY WORKSHOP
on**

**SIMULATION OF IEEE
TRANSACTION PAPERS ON
INDUSTRIAL ELECTRONICS**

24 - 25 January 2018

**Organized by
Department of EEE
Dr. Mahalingam College of
Engineering and Technology
Pollachi- 642 003, Tamil Nadu, INDIA
Phone: +91-4259-236030/40/50
Fax: +91-4259-236070
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ABOUT THE COLLEGE

Dr. Mahalingam College of Engineering and Technology (MCET) was established in the year of 1998 by Dr. M. MANICKAM with a view to commemorate the 75th birthday of his beloved father Arutchelvar Dr. N. MAHALINGAM with a mission to impart high quality competency based education in Engineering and Technology to the younger generation with the required skills and abilities to face the challenging needs of the industry around the globe. MCET is a self-financing, co-educational Autonomous Engineering College and it is approved by All India Council for Technical Education (AICTE), New Delhi & Affiliated to Anna University, Chennai. The Institution has been accredited by National Assessment and Accrediation Council (NAAC) . MCET currently offers 10 UG and 6 PG courses and 5 Doctoral programmes in Engineering and Technology.

ABOUT THE DEPARTMENT

The department of Electrical and Electronics Engineering was established in 2002 and offers B.E (EEE), M.E (Applied Electronics) Programmes and Ph.D. The department offers an excellent atmosphere for students to learn and acquire the necessary skills that are much sought after by the industry. A wide range of research activities carried out in the department, allow the students to gain contemporary knowledge about real-life problems in the industry.

ABOUT THE WORKSHOP

Simulation plays a very important role in designing complex systems such as power electronic circuits. A number of software packages are available to simulate power electronic circuits at the present time. With simulation, it is possible to quickly find out the effect of circuit parameters on the circuit performance, some of the time-consuming and expensive prototyping steps can be avoided. For these reasons, simulation has become an important component in academic and also in industrial design processes. In this course three IEEE transaction papers will be explained and simulated using MATLAB/SIMULINK.

IEEE Papers include:

- 1."A Self Balanced Step up Multilevel Inverter Based on Switched Capacitor Structure" IEEE transactions on Power Electronics, vol. 33, no. 1, January 2018."
- 2."Sensor-Less Five-Level Packed U-Cell (PUC5) Inverter Operating in Stand-Alone and Grid-Connected Modes" IEEE transactions on Industrial Informatics, vol. 12, no. 1, February 2016.
- 3."A Flying-Capacitor-Clamped Five-Level Inverter Based on Bridge Modular Switched-Capacitor Topology" IEEE transactions on Industrial Electronics, vol. 63, no. 12, December 2016.

ELIGIBILITY

Faculty members of Engineering Colleges, Research Scholars, Students and Industrialists can apply. Participants may limited to 30 persons.

IMPORTANT DATES

Last date for Registration : 20.01.2018
Intimation about Confirmation : 22.01.2018

REGISTRATION FEE WITH GST

Industry Person : Rs.1000/-
Academicians/ Research Scholars : Rs.800/-
IEEE Student Members : Rs.400/-
Non IEEE Student Members : Rs.500/-

Registration fee may be sent in the form of DD drawn in favour of "**MCET IEEE STUDENTS BRANCH**" payable at Pollachi along with the registration form duly filled in. Duly filled registration form along with DD must reach us on or before 20th January 2018.

ADDRESS FOR COMMUNICATION

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Associate Professor

Department of EEE

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Coimbatore District, Tamilnadu

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