Report on II Year Orientation Program: (2016-17)

Objective:

The objective of the Orientation Program organized for the first year, second semester students:

- 1. To identify the student's aspirations.
- 2. To emphasize skills, attributes and traits necessary for a successful career.
- 3. To create awareness amongst students on current industry trends and employment scenario.
- 4. To emphasize on Industry Expectations.
- 5. To develop students in building his career roadmap.

Preface:

MCET has become autonomous since 2011 and has been awarded Grade A by NAAC. As a progressive engineering institution MCET leveraging on its autonomous status has been introducing several innovative measures to enhance the skill development of the students resulting in augmentation of the employability aspect of students. As a first step management has clubbed the various disciplines under three broad streams of IT, Circuit and Mechanical-Civil branches to impart focused training and allow for inter-disciplinary learning.

In order to equip the students with the relevant industry oriented skills, a graded stream wise, student centric and credit linked training right from the second semester up to the sixth semester is planned. To set a tone for the above plan management conceived an orientation program after a series of brainstorming session with the CPG team and HODs. It was decided that circuit stream will invite at least 4 successful senior industry experts and at least 4 successful alumni to address the students and give them some pep input. With this broad idea the orientation program got off to a meaningful start.

The Industry Experts for the programme include,

- Mr.Mohan.K,Tech leader Diagnostic engineering, CISCO Systems &Inc, Bangalore
- 2. Mr.Charler Hardware Test Engineer, Robert Bosch Engineering, Coimbatore
- 3. Mr.JehadeasanHead—Computer division, Indira Gandhi Centre for Atomic Research, Chennai.
- 4. MrVikram Krishna, Senior Engineer, Juniper Networks, Bangalore

Mr.Charler Hardware test engineer. Robert Bosch Engineering and business solution limited, Coimbatore. He is an automotive test engineer who have us overview about hardware's used in the recent automotive cars. He insisted us to correlate our theory along with the practical applications. The session went quite interesting and bit interactive. He keenly advised us to the aware about datasheet interpretation. He also stated that an individual is known as skilled electronic engineer only when one is aware about the complete details of the electronics. Questionary session went very interactive students come up with various types of innovative questions and clarified their doubts with him. He insisted us to pursue online courses on software and hardware which might be very effective in our electronic career. His addressing was quite informative and very useful to the budding engineers.

Mr.Mohan.K,Tech leader – Diagnostic engineering, CISCO Systems & Inc, Bangalore. H initiated the session by introducing various new technologies of the era Google-soli was one among them. Next one was about SIPH (silicon photonics). This was based on that silicon does not emit light but LED'S are made up of silicon which gives out light. He also mentioned about scope of young engineers in the field of internet of things (IOT), which include newel chips. He states that fnm silicon fabrication is going to play a vital role in electronics. He gave us the information about the opportunities in which we can find beneficial platforms in future.

The areas covered by him

1.Embedded system firmware's, 2.Embedded system software's, 3.Networking engineers, 4.ASIC verification, 5.ASIC physical designer, 6.ASIC designers,7.ASIC packaging designer,8.RF Engineering,9.PCB layout designer,10.Signal integrity engineers,11.Hardware design engineers,12.Hardware test engineers. He ended up the session by invoking three aptitude questions which make us to think as a engineer. He also rewarded a pen drive to the blooming engineers who cracked his questions.

Mr.Vikram Krishna, The one of the guests in the STEP program for the second years of circuit stream which is conducted on 24 Dec 2016 (Saturday). He is an employee of 'Juniper Networks' in Bangalore. He is an alumnus of our college and graduated from Electronics and communication Engineering (ECE) department. He has a working experience of ten years. Apart from being professional, be gave out his opinions and suggestions as a super senior. He knew the pulses of engineering students and spoke accordingly. He recommended the students of circuit stream to take up core jobs instead of jobs in software companies. He spoke about the most emerging field of today's word which is nothing but 'Internet of Things (IOT)'. He told about the advancement of IOT which can improve the human standard of living. He didn't stop with that, he also told that, that IOT would soon become liable or hacking making the word more insecure. He also stressed on the importance of starting mini projects from 4th semester onwards which is current semester for us. He shared his experiences and difficulties of his miniproject when he was a student like us. As a senior he guided us to a path which is appropriate for today's engineering world.

Mr. Jehadeasan head —computer division, Indira Gandhi Centre for Atomic Research (IGCAR), Chennai, delivered an informative presentation about the atomic power production in IGCAR. He included the methods and steps in atomic power production which is very rare to see and know by the normal citizen. He also gave us information's about the future power generation with thorium breeder, which would make a revolution in power production. In India thorium is abundant, so its comparatively a letter process. He also shared with us how IGCAR selects its research scholars which nourished us with a thought of joining IGCAR. He also included the opportunities in design and development in the following field.

- 1.Basic design and qualification
- 2.FPGA/CPLD lared design
- 3. Safety critical / related systems
- 4. Safety and realility analysis

- 5. Verification of lased design
- 6. Hardware in fault management
- 7. Fiber optic sensor development
- 8. Development of field has communication.

During the panel discussion, Students get clarifications about the power generation in IGCAR and also discussed about the future techniques of atomic power production.