

ABOUT THE COLLEGE

Dr. Mahalingam College of Engineering and Technology (MCET) was established in the year 1998 by Dr.M.MANICKAM with a view to commemorate the 75th birth day of his beloved father Arutchelvar Dr. N. MAHALINGAM with a mission to impart high quality competency based education in Engineering & Technology to the younger generation to acquire the required skills and abilities to face the challenging needs of the industry around the globe. MCET is a self- financing, co-educational Autonomous Engineering College and it is approved by All India Council for Technical Education (AICTE), New Delhi & affiliated to Anna University, Chennai. The Institution has been accredited by the National Board of Accreditation (NBA), New Delhi and ISO 9001:2008 certified by TUV. MCET currently offers 10 UG 6 PG and 5 doctoral programmes in Engineering, Technology and Science.

ABOUT THE DEPARTMENT

The Department of Electronics and Communication Engineering was established in the year 1998 and offers B.E (ECE) and M.E(Communication Systems) approved by AICTE, affiliated to Anna University, and accredited by NBA. The department has experienced and qualified faculty members who are fully committed to teaching, research and dedicated to impart quality education to the students. The students have access to well equipped, state-of-the-art laboratories. The department is focusing on research activities in the predominant areas viz., RF, VLSI, image & signal processing, Communication and Networking.



Facilities Available		
*	CADENCE EDA SUITE	Analog, Digital and Mixed Signal Design
*	RECONFIGURABLE FPGA DESIGN	Spartan 6, Nexys 3 & Zynq SoC FPGA boards
*	XILINX - VIVADO DESIGN SUITE	High level Synthesis, IP Integrator and System Generator for Image & Signal Processing Applications
*	CADENCE ALLEGRO	PCB design solution.

ABOUT ASIC CENTRE OF EXCELLENCE

The centre was established in the year 2014 in collaboration with Cadence design systems (India) Ltd., Bangalore. Cadence design systems (India) Ltd., has authorized MCET as the sole proprietor to promote CADENCE EDA based IC design education for this region. ASIC centre has to its credit the state-of-the art CADENCE EDA Suite (20 user license). The centre conducts periodical Workshops and Short term training programmes to Students, Faculty members, Research scholars and Industrialists to promote VLSI education. In addition the centre houses advanced FPGA boards to offer HDL synthesis and implementation for Image & Signal Processing applications.



Workshop
On

**Custom IC Design, CMOS Process
Integration, IC Fabrication,
Packaging and Testing
(23rd February, 2018)**

Organized by

**Department of
Electronics & Communication
Engineering**



**Dr. MAHALINGAM COLLEGE OF
ENGINEERING AND TECHNOLOGY**
(An Autonomous Institution)
(Accredited by NBA, NAAC with 'A'
Grade Institution)

Pollachi-642003
Coimbatore (Dt), Tamilnadu
Phone: +91-4259-236030/40/50

Fax: +91-4259-236070
Website: www.mcet.in

ABOUT THE WORKSHOP

The objective of the workshop is to disseminate the recent trend in IC design with lower geometry in analog and mixed signal disciplines, fabrication & packaging issues and Testing to UG & PG students of circuit stream. At the successful completion of the workshop participants will get an in-depth knowledge in analog and mixed signal system design using PDK components, generating layouts for fabrication, packaging issues and testing.

TOPICS TO BE COVERED

The topics of interest are given below, but not limited to

- Introduction to System design
- Analog IC Design in 180nm Technology
- CMOS Process Integrations in lower geometry
- PDK- components and how to use
- IC fabrication and Packaging
- Testing the IC

TECHNICAL EXPERTS FOR THE WORKSHOP

1. Shri H.S. Jatana,
Group Head - DPG
2. Shri. Ashutosh yadav,
Technical Head,
Semi Conductor Laboratories(SCL),
Department of Space, Govt. of India,
Chandigarh.

ORGANIZING COMMITTEE:

Convener:

Dr. R.Sudhakar
Professor and Head / ECE

Co-Ordinator:

Dr. K. N. Vijeyakumar
Associate Professor / ECE

Co- Coordinators:

Dr.B.Vinothkumar, AP(SG) / EEE
Ms. S. Kalaiselvi, AP/ECE
Ms. C. Kalamani, AP/ECE
Mr. B. Pradeepkumar, AP/ECE
Mr. H. MaheshKumar, AP/ECE
Ms. P. Sathyabama, AP/ECE

Contact Persons :

Mr. B. Pradeepkumar, AP/ECE

Mr. H. MaheshKumar, AP/ECE

Mobile :7094022255, 9865492116

REGISTRATION DETAILS:

Category	Registration fee
UG/PG students	Rs. 300/-

REGISTRATION FORM

**Workshop
On**

**“Custom IC Design, CMOS
Process Integration, IC
Fabrication, Packaging and
Testing”**

(23rd February, 2018)

Name:.....

Department:.....

Institution:...Dr.MCET, Pollachi.

Address:.....

.....

.....

Pincode:.....

Phone/Mobile:.....

E-mail:.....

Place:

Signature of the Applicant