

Academic year: 2016-2017

Date(s)	Type of Programme	Title	No. of Participants attended		Total	Resource Persons
			External	Internal		
15.09.2016 & 16.09.2016	Workshop	Custom IC design using CADENCE EDA tool	13	-	13	Dr.K.N.Vijeyakumar Asso. Prof/EEE Ms.K.Saranya & Ms.M.Sangeetha, AP/EEE Ms.S.Kalaiselvi, AP /ECE
30.09.2016	Workshop	Custom Analog IC design using CADENCE EDA tool	15	-	15	Dr.K.N.Vijeyakumar Asso. Prof/EEE Ms.K.Saranya & Ms.M.Sangeetha, AP/EEE Ms.S.Kalaiselvi, AP/ECE
01.10.2016 & 02.10.2016	Workshop	Recent Trends in Physical Design and Custom IC Design using Cadence EDA Suite	-	52	52	Mr. Nuthankumar, Senior physical design Engineer, Intel Corporation Ltd, Bangalore.
30.03.2017	Hands on training	Design and Analysis of Amplifier Circuits using Cadence EDA Tool	-	19	19	Dr.K.N.Vijeyakumar, Asso. Prof/EEE, Dr.B.Vinothkumar, AP(SG)/EEE Ms.K.Saranya & Ms.M.Sangeetha, AP/EEE Ms.S.Kalaiselvi, AP/ECE
31.03.2017	Hands on training	Design and Analysis of Amplifier Circuits using Cadence EDA Tool	-	15	15	Dr.K.N.Vijeyakumar, Asso. Prof/EEE, Dr.B.Vinothkumar, AP(SG)/EEE Ms.K.Saranya & Ms.M.Sangeetha, AP/EEE Ms.S.Kalaiselvi, AP/ECE