

**VALUE ADDED COURSES:2016-2017**

<b>Date(s)</b>	<b>VAC Course Name</b>	<b>No. of students Benefitted</b>	<b>Resource Persons</b>
17.12.2016 to 22.12.2016	CMOS Analog IC Design Using Cadence EDA Tool	26	Dr.K.N.Vijeyakumar, Asso. Prof/EEE Ms.K.Saranya & Ms.M.Sangeetha, AP/EEE Ms.S.Kalaiselvi, AP /ECE
13.02.2017 to 17.02.2017	CMOS Analog IC Design and Recent trends in Physical Design using Cadence EDA Tool	29	Dr.K.N.Vijeyakumar, Asso. Prof/EEE, Dr.B.Vinothkumar, AP(SG)/EEE Ms.K.Saranya & Ms.M.Sangeetha, AP/EEE Ms.S.Kalaiselvi, AP /ECE
15.03.2017 to 17.03.3017	CMOS Analog RF IC Design Using Cadence EDA Tool	17	Dr.K.N.Vijeyakumar, Asso. Prof/EEE, Dr.B.Vinothkumar, AP(SG)/EEE Ms.K.Saranya& Ms.M.Sangeetha, AP/EEE Ms.S.Kalaiselvi, AP /ECE