

Academic Year: 2015-2016

UG Projects:

Domain	S.No	Name of the Student	Guide Name	Title of the project
Analog and Mixed IC Design	1	Balavenkateshwaran.C	Dr.K.N.Vijeyakumar, M.E.,Ph.D., (AssociateProf/ECE)	ASIC Implementation of High Speed Flash ADC
	2	Nandhinipriya.R	Dr.K.N.Vijeyakumar, M.E.,Ph.D., (AssociateProf/ECE)	Design of Low Power Hybrid Full Adder using Data Driven Dynamic Logic and Pass Transistor Logic

PG Projects:

Domain	S.No	Name of the Student	Guide Name	Title of the project
Analog and Mixed IC Design	1	Pavithran.B	Dr.K.N.Vijeyakumar, Asso.Prof./ECE	ASIC Implementation of Reversible Arithmetic Unit
	2	Subha Devi.M	Dr.K.N.Vijeyakumar, Asso.Prof./ECE	VLSI Implementation of High Speed Area Efficient Reconfigurable Data Path Unit for Floating Point Application
Digital IC Design	3	Priyanka.L	Ms.K.Saranya AP/EEE	ASIC Design of High Speed Radix-10 Decimal Multiplier and its Implementation in Processing Unit